

Robert Canright List of Publications

Books

Achieve Lasting Happiness, Timeless Secrets to Transform Your Life

by Robert E. Canright, Jr.

AuthorHouse, Bloomington, Indiana, 2005, ISBN 1-4208-3894-6

This is the only book that brings Confucianism into American culture. It includes a guided 28 day study of the Analects of Confucius.

Packaging of High-Speed Digital Electronic Systems

by Robert Canright, et al

Chapter 11, pp. 11.1 to 11.76 in

Electronic Packaging and Interconnection Handbook, Charles A. Harper, editor

McGraw-Hill, New York, 1991, ISBN 0-07-026684-0

Technical Papers

Controlled Reflection Design in High Speed Computers, Robert E. Canright, Jr., Proceedings of the 1994 International Symposium on Microelectronics, pp. 112 - 117 (sponsored by ISHM, the International Society for Hybrid Microelectronics). This paper examines designing a digital transmission line and the integrated circuit output driver as a system, including the non-linear output impedance of the output driver.

Practical Design for Controlled Impedance, Robert E. Canright, Jr., Proceedings of the 41st Electronic Components and Technology Conference, 1991, pp. 370 – 377, sponsored by the IEEE (Institute of Electrical and Electronics Engineers).

A Simple Formula for Dual Stripline Characteristic Impedance, Robert E. Canright, Jr., IEEE Southeastcon '90 Proceedings, pp. 903 – 905, April 1990.

Comments on “An Analytic Algorithm for Unbalanced Stripline Impedance”, Robert E. Canright, Jr., IEEE Transactions on Microwave Theory and Technique, Vol. 40, No. 1, January 1992, pp. 177 – 179

Capacitance: Relationships and Measurements, Robert E. Canright, Jr., Proceedings of the 40th Electronic Components and Technology Conference, Vol. 1, 1990, pp. 163 – 168. This paper clarifies the relationships between 3 representations of capacitance: lumped element capacitance, Maxwell's capacitance, and two-terminal capacitance.

The H-Tree Structure, Impedance matching for tapped and branched transmission lines, Robert E. Canright, Jr., Printed Circuit Design magazine, January 1992, pp. 42 – 44.

Equations for Crosstalk and Controlled Impedance, Robert E. Canright, Jr., Proceedings of the Technical Conference, Eighth Annual International Electronics Packaging Conference, Dallas, Texas, November 7 – 10, 1988, pp.509 – 520.

A Formula to Model Delta-I Noise, Robert E. Canright, Jr., Proceedings of the 37th Electronic Components and Technology Conference, 1987, pp. 354 – 360.

Reflections of High Speed Signals Analyzed as a Delay in Timing for Clocked Logic, Robert E. Canright, Jr. and Arden R. Helland, Proceedings of the 24th ACM/IEEE Design Automation Conference, 1987, pp. 133 – 139.

Maximizing Tolerances for PWB Characteristic Impedance, Robert E. Canright, Jr., Proceedings of the Third IEEE/CHMT International Electronic Manufacturing Technology Symposium, Anaheim, Calif., October 12 – 14, 1987, pp. 213 – 219. I was also invited to present this paper at an IEEE conference in Paris, but my employer would not send me to Paris.

Simulating and Controlling the Effects of Transmission Line Impedance Mismatches, Robert E. Canright, Jr., Proceedings of the 23rd ACM/IEEE Design Automation Conference, June 29 – July 2, 1986, pp. 778- 785.

Predicting Effects of Transmission Line Impedance Mismatches, Robert E. Canright, Jr., Proceedings of the 36th Electronic Components Conference, 1986, pp. 169 – 176.

Considerations for High-Speed Digital Packaging, Robert E. Canright, Jr., Proceedings of the Technical Conference, Fifth Annual International Electronics Packaging Conference, October 21 – 23, 1985, Orlando, Florida, pp. 18 – 32.